

METHOD FOR FABRICATING A THIN LINE STRUCTURE, MULTILAYERED STRUCTURE AND MULTILAYERED INTERMEDIATE STRUCTURE

BACKGROUND OF THE INVENTION

Field of the Invention

[0001] This invention relates to a method for fabricating a thin line structure and a multilayered structure containing the thin line structure which are preferably usable in semiconductor device engineering. This invention also relates to a multilayered intermediate structure which is usable for fabricating the thin film structure and the multilayered structure.

Description of the related art

[0002] In the fabrication of a wiring pattern in a semiconductor element, first of all, a minute pattern is made by means of photoresist and then, the wiring pattern is made by means of deposition of a metallic layer commensurate with the photoresist pattern. In the fabrication of a multilayered wiring pattern, a plurality of wiring patterns are made and then, a plurality of protective insulating layers are made in order to electrically insulate the adjacent wiring patterns, respectively.

[0003] In the conventional process, however, many fabricating steps are required, so that the total fabricating process becomes complicated. In addition, in the fabrication of the multilayered wiring structure, it is required that an upper wiring pattern is formed precisely on a lower wiring pattern, so that the total fabricating process of the multilayered wiring structure becomes difficult.

[0004]

SUMMARY OF THE INVENTION

It is an object of the present invention to form a thin line structure such as a wiring pattern by means of easy means.

[0005] For achieving the above object, this invention relates to a method for fabricating a thin line structure, comprising the steps of:

preparing a given silicon base,

forming, on the silicon base, a strain-relaxed silicon germanium layer with penetrated dislocations,

forming a metallic layer on the silicon germanium layer to form a

multilayered intermediate structure which is made of the silicon base, the silicon germanium layer and the metallic layer,

and

heating the multilayered intermediate structure to diffuse metallic elements of the metallic layer through the penetrated dislocations of the silicon germanium layer and to form a thin line structure made of metallic silicide at a boundary face between the silicon base and the silicon germanium layer.

[0006] This invention also relates to a multilayered intermediate structure comprising:

a given silicon base,

a strain-relaxed silicon germanium layer with penetrated dislocations which is formed on the silicon base,

and

a metallic layer which is formed on the silicon germanium layer.

[0007] The inventors had intensely studied to achieve the above-mentioned objects. As a result, they found out the following fact of the matter.

First of all, a multilayered intermediate structure is made of a silicon base, a strain-relaxed silicon germanium layer with penetrated dislocations and a metallic layer, and heated to diffuse metallic elements of the metallic layer to the boundary face between the silicon base and the silicon germanium layer through the penetrated dislocations of the silicon germanium layer. In this case, the metallic elements are reacted with silicon elements of the silicon base and/or silicon germanium layer to form a thin line structure made of metallic silicide at the boundary face.

[0008] According to the present invention, therefore, the intended thin line structure can be formed at the boundary face, that is, on the silicon base only by the above-mentioned strain-relaxed silicon germanium layer without a conventional complicated pattern-forming technique. If necessary, the silicon germanium layer can be peeled off by means of a conventional technique such as lift-off means or etching means, to expose the thin line structure. As a result, various semiconductor elements can be made by utilizing the thin line structure of the present invention.

[0009] The strain-relaxed silicon germanium layer can be made by means of

a conventional epitaxial growth technique if the germanium content of the silicon germanium layer is adjusted appropriately. As the germanium content of the silicon germanium is increased, the difference in lattice constant between the silicon base and the silicon germanium layer is increased, so if the silicon germanium layer with large amount of germanium is formed on the silicon base, misfit dislocations are created at the boundary face between the silicon base and the silicon germanium layer in order to relax the difference in lattice constant therebetween.

[0010] The misfit dislocations are propagated upward with the progress of the epitaxial growth of the silicon germanium layer to form penetrated dislocations in the epitaxially grown silicon germanium layer, and thus, complete the strain-relaxed silicon germanium layer.

[0011] The density of the thin line structure, formed at the boundary face between the silicon base and the silicon germanium layer, depends on the density of the penetrated dislocations of the silicon germanium layer. Therefore, it is required to design the density of the penetrated dislocations so as to realize the intended density of the thin line structure. Concretely, in order to design the intended density of the penetrated dislocations, the forming method and the forming condition of the silicon germanium layer are adjusted, and the germanium content of the silicon germanium layer is adjusted. In order to change the density of the penetrated dislocations widely, it is desired to control the germanium content of the silicon germanium layer.

Other features and advantages of the present invention will be described hereinafter.

BRIEF DESCRIPTION OF THE DRAWINGS

For better understanding of the present invention, reference is made to the attached drawings, wherein

Fig. 1 is a cross sectional view showing one step in a fabricating method of multilayered structure according to the present invention,

Fig. 2 is a cross sectional view showing the step after the step shown in Fig. 1,

Fig. 3 is a schematic view showing a multilayered structure according to the present invention, and

Fig. 4 is a cross sectional TEM photograph of a multilayered structure according to the present invention.

[0012]

DESCRIPTION OF THE PREFERRED EMBODIMENTS

This invention will be described in detail hereinafter.

Figs. 1 and 2 are explanatory views for a fabricating method of thin line structure according to the present invention. As shown in Fig. 1, first of all, a silicon base 11 is prepared, and a silicon germanium layer 12 is epitaxially grown on the silicon base 11 by means of a conventional film-forming technique. In this case, the silicon base 11 is heated within 400-700°C.

[0013] Since a given amount of germanium is contained in the silicon germanium layer 12, the lattice constant of the silicon germanium layer 12 becomes larger than the lattice constant of the silicon base 11. In order to epitaxially grow the silicon germanium layer 12 on the silicon base 11, it is required to compensate the difference in lattice constant between the silicon base 11 and the silicon germanium layer 12. In this case, misfit dislocations are created at the boundary face 15 between the silicon base 11 and the silicon germanium layer 12, and then, propagated through the epitaxially growing layer 12, to form penetrated dislocations 14 in the epitaxially grown layer 12.

[0014] Then, a metallic layer 13 is formed on the silicon germanium layer 12 to form a multilayered intermediate structure 10, which is heated. In this case, metallic elements of the metallic layer 13 are diffused to the boundary face 15 through the penetrated dislocations 14, and chemically reacted with silicon elements of the silicon base 11 and/or the silicon germanium layer 12, to form a thin line structure 16 made of metallic silicide at the boundary face 15 and thus, to form a multilayered structure 20 made of the silicon base 11, the silicon germanium layer 12 and the thin line structure 16.

[0015] In Fig. 2, although the metallic layer 13 disappeared, it may be rendered partially remained.

[0016] If necessary, the silicon germanium layer 12 can be peeled off by means of a conventional technique such as a lift-off method or an etching method, to expose the thin line structure 16. As a result, various semiconductor elements can be fabricated by utilizing the thin line structure 16.

[0017] The density of the thin line structure 16 at the boundary face 15 is not restricted, and can be controlled by adjusting the density of the penetrated dislocations 14. The density of the penetrated dislocations 14 is not restricted, but preferably set within 10^{-12} - 10^{-8} /cm². In this case, the thin line structure 16 can be formed precisely in high density.

[0018] As mentioned above, the density of the penetrated dislocations 14 can be adjusted by controlling the forming method and the forming condition of the silicon germanium layer 12. The density of the penetrated dislocations 14 can be also adjusted by controlling the germanium content of the silicon germanium layer 12. In order to change the density of the penetrated dislocations 14 widely, it is desired to control the germanium content of the silicon germanium layer 12. In order to realize the above-mentioned range of the density of the penetrated dislocations 14, the germanium content is preferably set within 20-70 atomic percentages.

[0019] Although the penetrated dislocations 14 are created along various crystal orientations of the silicon base 11, if the forming condition and the like of the silicon germanium layer 12 are controlled, they can be created mainly along the crystal orientation <011> of the silicon base 11. Therefore, the thin line structure 16 can be formed regularly at the boundary face 15 along the same <011> crystal orientation.

[0020] The metallic layer 13 can be made of any kind of metallic material, but preferably made of a metallic material containing metallic elements with large affinity for silicon elements. In this case, through the chemical reaction between the silicon elements of the silicon base 11 and/or the silicon germanium layer 12 and the metallic elements of the metallic layer 13, the separation of the silicon elements from the base 11 and layer 12 is facilitated, and thus, the thin line structure 16 can be formed easily.

[0021] As the metallic elements are exemplified Ni elements, Co elements, Ti elements, Pt elements, Fe elements and Pd elements. If the metallic layer 13 contains such metallic elements as mentioned above, the thin line structure 16 can be easily made of a metallic silicide containing the metallic elements.

[0022] The above-mentioned metallic elements is like to be epitaxially grown in the silicon base 11, so the thin line structure 16 is also like to be formed in the

silicon base 11. After the formation of the thin line structure 16, therefore, the boundary face 15 can be maintained plane in atomic level, so that the resultant multilayered structure is rendered very exquisite.

[0023] Among the metallic elements as mentioned above, the Ni elements can be preferably employed because Ni is generally employed as an electrode material for semiconductor elements. In this case, the thin line structure 16 is made of nickel silicide containing NiSi phase and/or NiSi₂ phase shifted from NiSi phase.

[0024] Fig. 3 is a perspective view schematically showing the multilayered structure 20 fabricated through the above-mentioned process. As shown in Fig. 3, if the thin line structure 16 is formed in lattice, a pattern 18 made of the metallic silicide can be formed on the silicon base 11.

[0025]

Example:

This invention will be described in detail, with reference to a concrete embodiment.

(Example)

First of all, a (100) silicon base was prepared, and heated at 600°C. Then, a Si_{0.53}Ge_{0.47} layer was epitaxially grown in a thickness of about 500nm on the silicon base by means of CVD. Then, a Ni layer was formed in a thickness of 20nm on the Si_{0.53}Ge_{0.47} layer by means of deposition using an electron gun, to form a multilayered intermediate structure, which was successively heated at 750°C under nitrogen atmosphere until the Ni layer disappeared. In this case, the Ni elements of the Ni layer was diffused to the boundary face between the silicon base and the Si_{0.53}Ge_{0.47} layer through the penetrated dislocations of the Si_{0.53}Ge_{0.47} layer.

[0026] Fig. 4 is a cross sectional TEM photograph of the resultant multilayered structure fabricated through the above-mentioned process. It was confirmed in Fig. 4 that a thin line structure was made of nickel silicide with NiSi₂ phase at the boundary face between the (100) silicon base and the Si_{0.53}Ge_{0.47} layer. According to the present invention, it is turned out that the intended thin line structure can be easily made only by the strain-relaxed silicon germanium layer with the penetrated dislocations without a conventional

complicated technique.

[0027] Although the present invention was described in detail with reference to the above examples, this invention is not limited to the above disclosure and every kind of variation and modification may be made without departing from the scope of the present invention.